IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

n re Application of:

Serial No.: 10/052,055

JASON A. GROVER

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Examiner:

Group Art Unit:

Evan T. Pert

Conf. No.:

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Atty. Dkt.:

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CUSTOMER NO.: 23720

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DETERMINING A POSSIBLE

CAUSE OF A FAULT IN A

SEMICONDUCTOR

Alexandria, VA 22313-1450

FABRICATION PROCESS

RESPONSE TO RESTRICTION REQUIREMENT DATED SEPTEMBER 24, 2003

Commissioner for Patents
P.O. Box 1450

CERTIFIE

DATE OF DEPOSIT

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Sir:

CERTIFICATE OF MAILING UNDER 7 CER. § 18

DATE OF DEPOSIT: October 9, 2003 CER. § 18

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This paper is submitted in response to the Restriction Requirement dated September 24, 2003, for which the one-month date for response is October 24, 2003.

In response to the restriction requirement issued by the Examiner, Applicants elect, with traverse, to prosecute claims 8-37, *i.e.*, the Group II claims.

It is believed that no fee is due; however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be required for any reason relating to this document, the Assistant Commissioner is authorized to deduct said fees from Advanced Micro Devices, Inc. Deposit Account No. 01-0365/TT4578. In

the event the monies in that account are insufficient, the Assistant Commissioner is authorized to withdraw funds from Williams, Morgan & Amerson, P.C. Deposit Account No. 50-0786/2000.087200.

The Examiner is invited to contact the undersigned attorney at (713) 934-4064 with any questions, comments or suggestions relating to the referenced patent application.

Respectfully submitted,

WILLIAMS, MORGAN & AMERSON, P.C. CUSTOMER NO. 23720

Date: October 9, 2003

By:

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